

To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

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Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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RENESAS TECHNICAL UPDATE

Classification of Production	MPU & MCU		No.	TN-H8*-257A/E	Rev.	1
THEME	Usage Note Updates for HD64F38004		Classification of Information	1. Spec change 2. Supplement of Documents ③ 3. Limitation of Use 4. Change of Mask 5. Change of Production Line		
PRODUCT NAME	HD64F38004	Lot No.	Reference Documents	H8/3802 Series, H8/38004 Series Hardware Manual REJ09B0024-0300Z Rev. 3.0	Term of Validity	
		All			Permanent	

Since the following bug is detected in the HD64F38004, we request you make the additional corrective measure.

1. Detected Bug

- Vector 17 (reserved for system use) interrupt requests may be generated, according to the power-on circuit state.
- When the I bit in CCR is cleared to 0, the above mentioned interrupt is accepted in the same way as any other interrupt. Because of this, exception handling begins, however there is no way to clear the interrupt request flag and program process is halted.

2. Corrective Measure

- To correct this error, add the program shown in figure 1. Figure 2 shows an example of this program in assembly language, and figure 3 shows an example in C language.

For the masked ROM version, the program with this process added does not need to modify or you can delete this processing part.

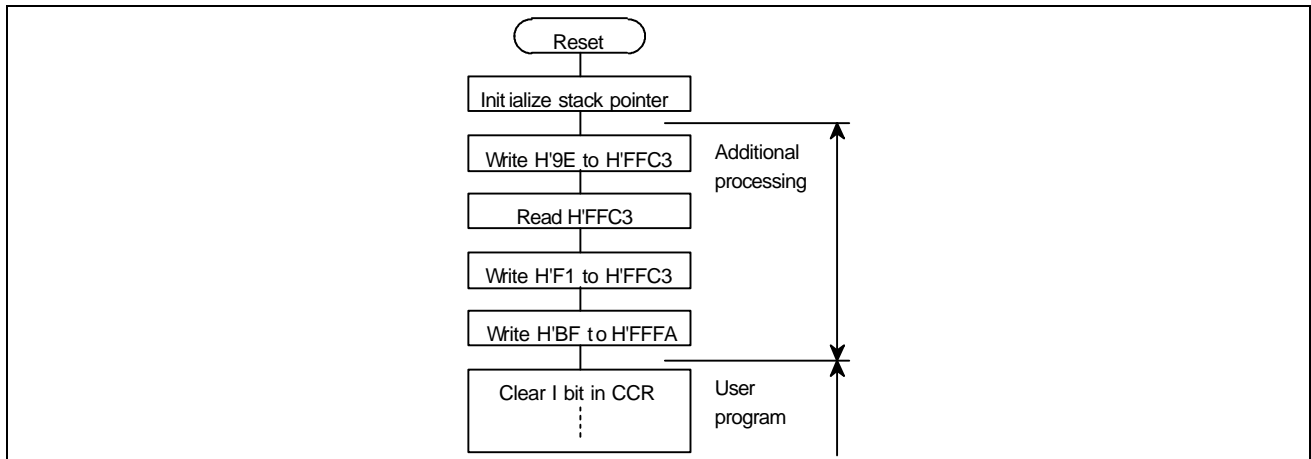


Figure 1 Additional Processing

```

.ORG      H'0000
.DATA.W   INIT
.ORG      H'0100
INIT:
MOV.W     #H'FF80:16,SP

MOV.B     #H'9E:8,R0L
MOV.B     R0L,@H'FFC3:8
MOV.B     @H'FFC3:8,R0L
MOV.B     #H'F1:8,R0L
MOV.B     R0L,@H'FFC3:8
MOV.B     #H'BF:8,R0L
MOV.B     R0L,@H'FFFA:8

ANDC.B    #H'7F:8,CCR          ; user program
  
```

Figure 2 Example of Program (Assembler)

```

void PowerON_Reset(void)
{
// -----
    unsigned char dummy;
    *((volatile unsigned char *)0xffc3)= 0x9e;
    dummy = *((volatile unsigned char *)0xffc3);
    *((volatile unsigned char *)0xffc3)= 0xf1;
    *((volatile unsigned char *)0xfffa)= 0xbf;
// -----
    set_imask_ccr(0);          // clear I bit
                                // user program
}
  
```

Figure 3 Example of Program (C Language)