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Renesas Electronics Corporation

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Tiny/Super Low Power E7 Emulator

Additional Document for User's Manual
Notes on Connecting the H8/38327F
and H8/38347F

Renesas Microcomputer Development
Environment System

H8 Family / H8/300H Tiny Series

Tiny/Super Low Power E7 HS0007TCU01HEP9

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Section 1 Connecting the Emulator with the User System

Before connecting an E7 emulator (hereafter referred to as emulator) with the user system, a connector must be installed in the user system so that an user system interface cable can be connected. When designing the user system, refer to the connector and recommended circuits shown in this manual.

Before designing the user system, be sure to read the E7 emulator user's manual and the hardware manual for related MCUs.

Table 1.1 shows the recommended connector for the emulator.

Table 1.1 Recommended Connector

Type Number	Manufacturer	Specifications
2514-6002	3M Limited	14-pin straight type

Connect pins 2, 4, 6, 10, 12, and 14 of the user system connector to GND firmly on the PCB. These pins are used as electrical GND and to monitor the connection of the user system connector. Note the pin assignments of the user system connector.

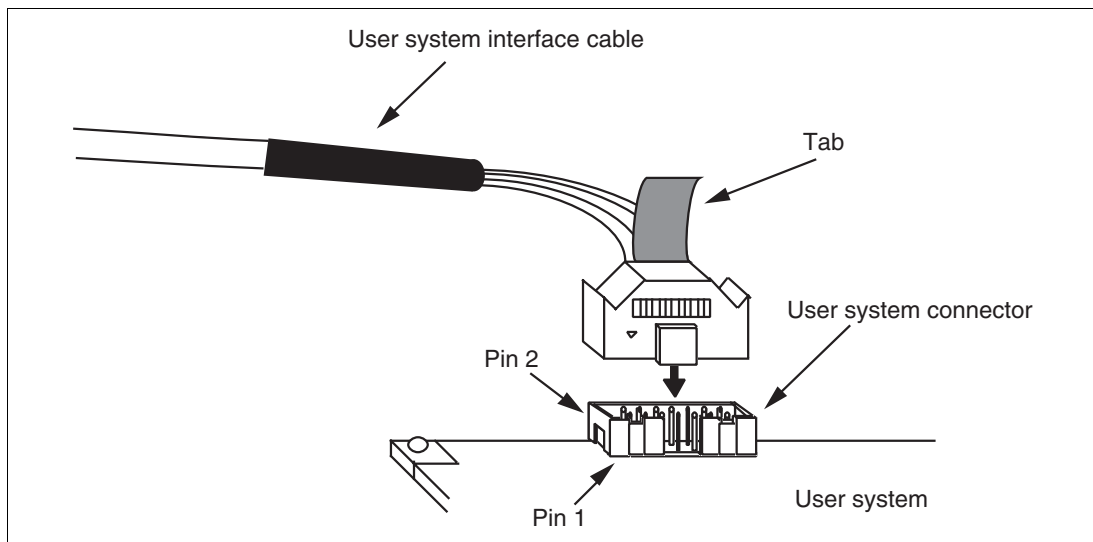


Figure 1.1 Connecting the User System Interface Cable to the User System

- Notes:
1. The pin number assignments of the 14-pin connector differ from those of the E10A emulator; however, the physical location is the same.
 2. Do not place any components within 3 mm of the connector.
 3. When the emulator is used in the writer mode, connect the emulator similarly to the user system.

Section 2 Pin Assignments of the E7 Connector

Figure 2.1 shows the pin assignments of the connector.

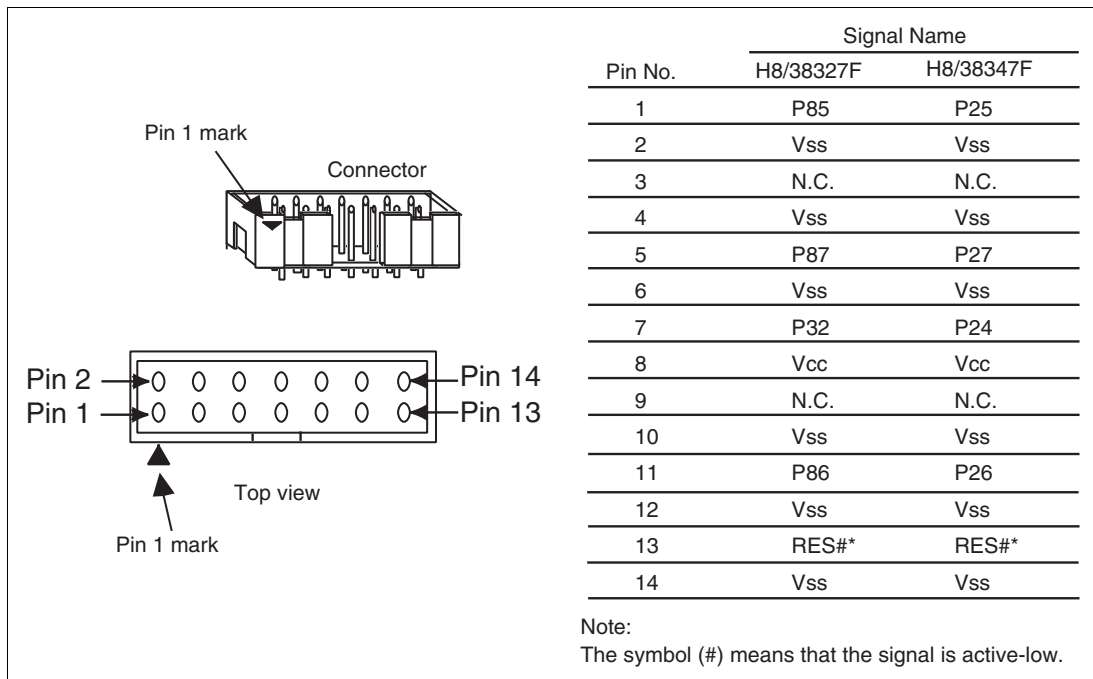


Figure 2.1 Pin Assignments of the Connector

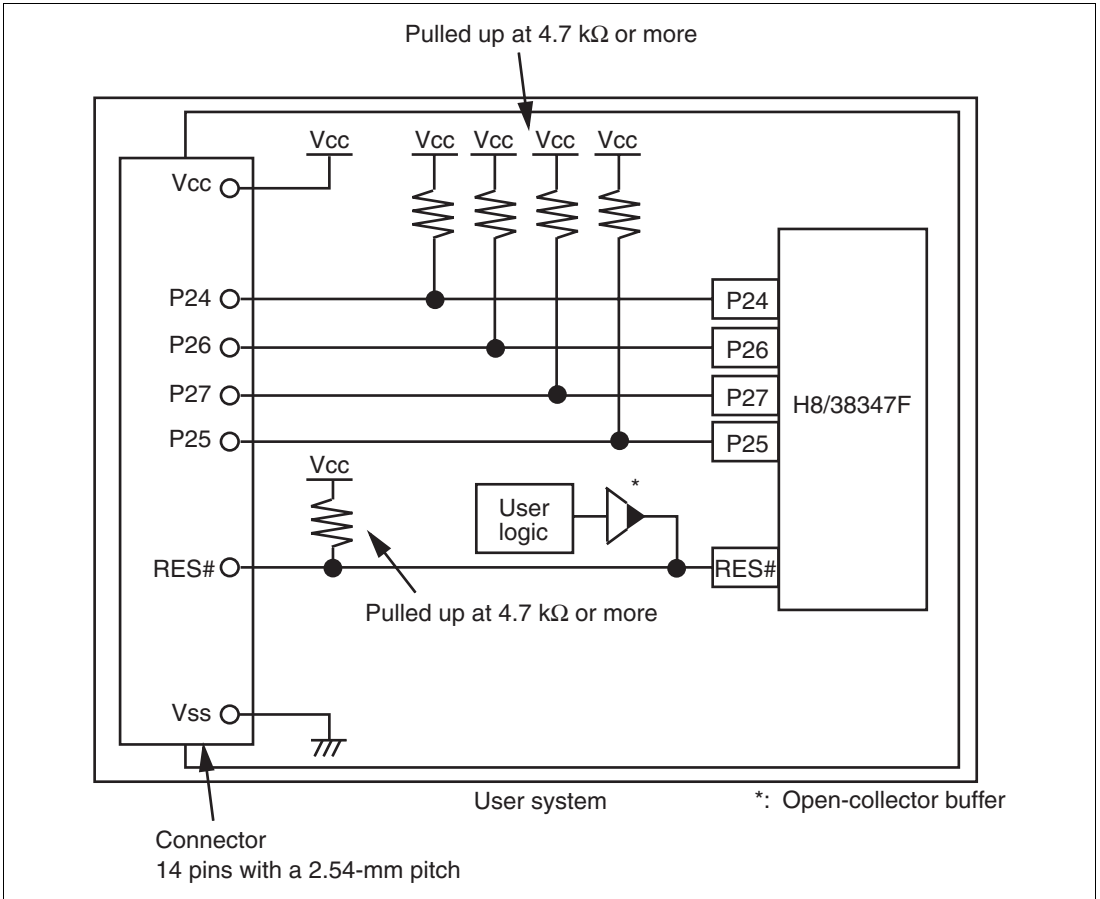


Figure 3.2 Example of Emulator Connection (H8/38347F)

Notes: 1. P85 to P87 pins (H8/38327F) and P25 to P27 pins (H8/38347F) are used by the emulator. Pull up and connect the emulator and MCU pins.

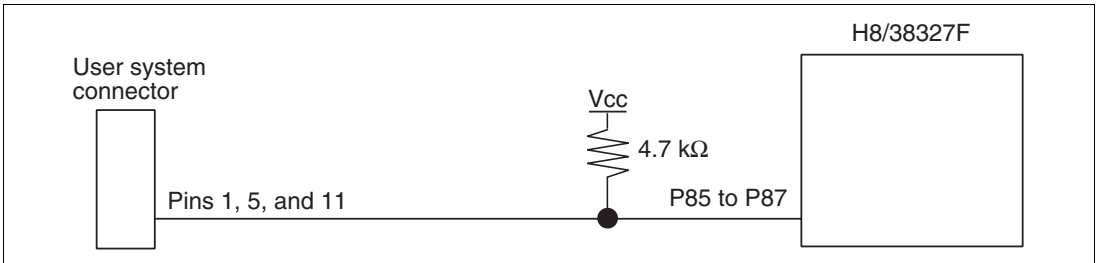


Figure 3.3 Connection of Emulator and P85 to P87 Pins (H8/38327F)

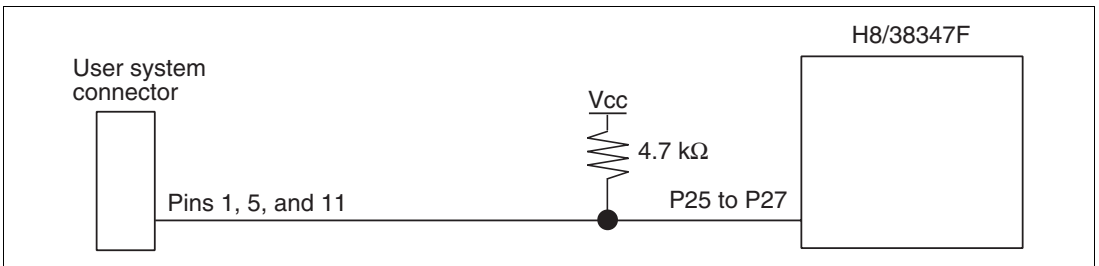


Figure 3.4 Connection of Emulator and P25 to P27 Pins (H8/38347F)

2. The P32 pin (H8/38327F) and P24 pin (H8/38347F) are used for forced break control by the emulator. Connect the emulator and MCU pins directly.

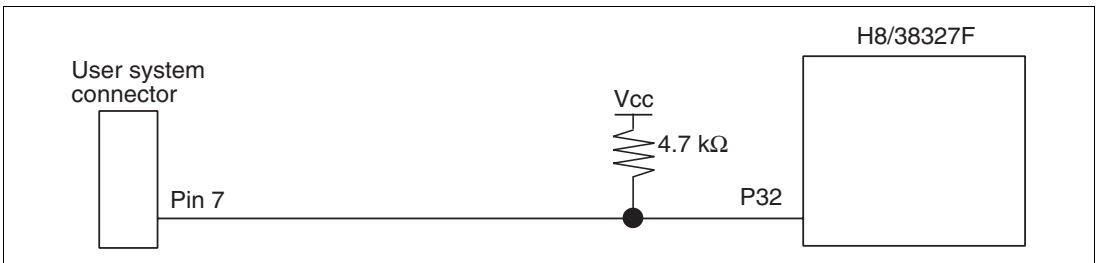


Figure 3.5 Connection of Emulator and P32 Pin (H8/38327F)

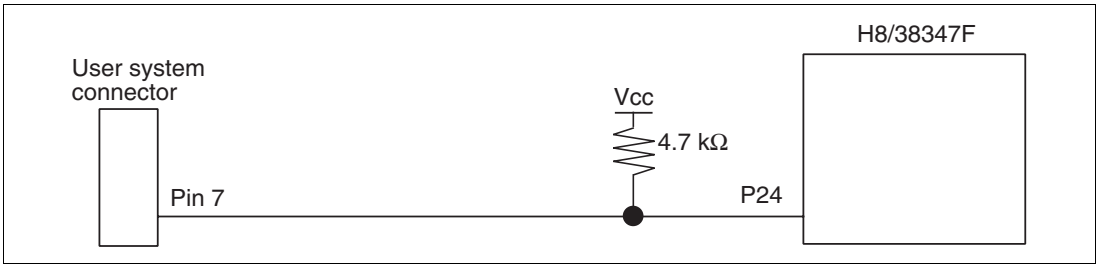


Figure 3.6 Connection of Emulator and P24 Pin (H8/38347F)

3. The RES# pin is used by the emulator. Create the following circuit by connecting the open-collector output buffer so that reset input can be accepted from the emulator.

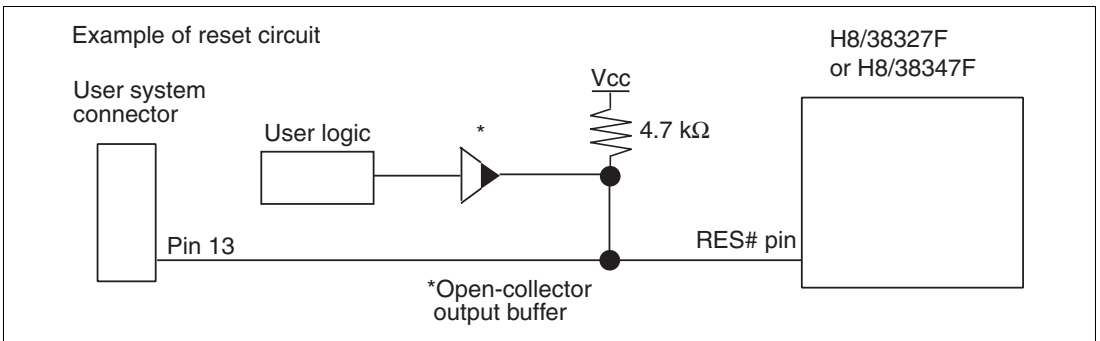


Figure 3.7 Example of a Reset Circuit

4. Connect Vss and Vcc with the Vss and Vcc of the MCU, respectively.
5. Connect nothing with N.C.
6. The input voltage, Vcc, must be connected to the user system Vcc (power supply). The amount of voltage permitted to input to Vcc must be within the guaranteed range of the microcomputer.
7. Figure 3.8 shows the interface circuit in the emulator. Use this figure as a reference when determining the pull-up resistance value.

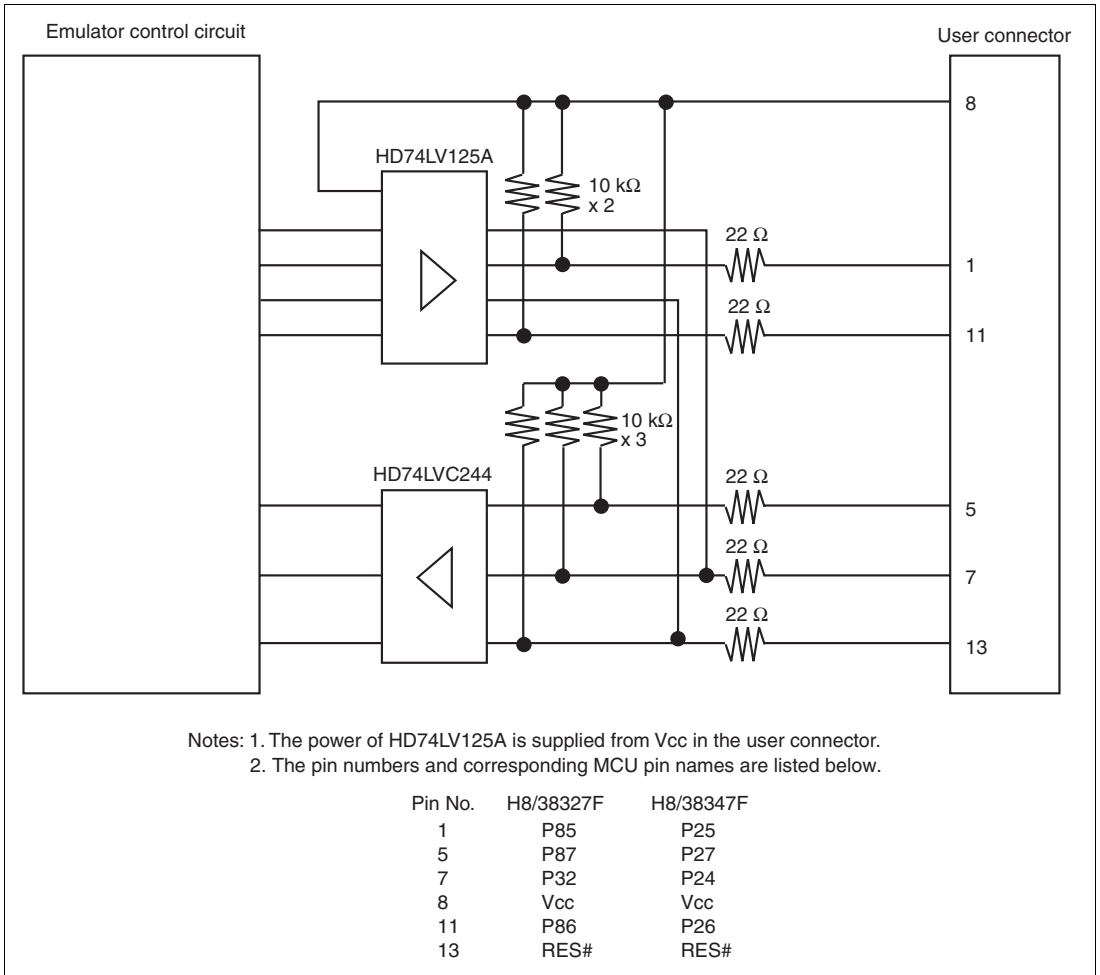


Figure 3.8 Interface Circuit in the Emulator (Reference)

Section 4 Differences between the MCUs and the Emulator

1. When the emulator system is initiated, it initializes the general registers and part of the control registers as shown in table 4.1.

Table 4.1 Register Initial Values at Emulator Power-On

Register	Initial Value
PC	Reset vector value in the vector address table
R0 to R6	H'0000
R7	H'FF80
CCR	H'80

2. Low-Power Mode

During a user program break, the CPU operating frequency is forced to a system clock (ϕ) for high-speed operation.

3. RES# Signal

The MCU signals are only valid during user program execution started with clicking the GO or STEP-type button. During a user program break, the RES# signal is not sent to the MCUs.

Note: Do not start user program execution or access the memory while control input signal (RES#) is being low. A TIMEOUT error will occur.

4. System Control Register

In the emulator, the internal I/O registers can be accessed from the [IO] window. However, be careful when accessing the system control register. The emulator saves the register value of the system control register at a break and returns the value when the user program is executed. Since this is done during a break, do not rewrite the system control register in the [IO] window.

5. Memory Access during Emulation

If the memory contents are referenced or modified during emulation, realtime emulation cannot be performed because the user program is temporarily halted.

6. The emulator communicates with the MCUs by using the P85 to P87 and P32 (H8/38327F), P24 to P27 (H8/38347F), and RES# pins. These pins except for RES# cannot be used.

7. **Sum Data Displayed in the Writing Flash memory Mode**
Sum data, which is displayed in the ‘Writing Flash memory’ mode, is a value that data in the whole ROM areas has been added by byte.
8. **Note on Executing the User Program**
The set value is rewritten since the emulator uses flash memory and watchdog timer registers during programming (Go, Step In, Step Out, or Step Over) of the flash memory.
9. **The power consumed by the MCU can reach several milliamperes.** This is because the user power supply drives one HD74LV125A to make the communication signal level match the user-system power-supply voltage. The power consumed rises little during user program execution since the emulator does not perform communication; it rises more during a break.
10. **Program Area for the Emulator**
Do not access a part of areas in the flash memory or the internal RAM since the emulator program uses these areas. If the contents of the program area for the emulator are changed, the emulator will not operate normally. In this case, restart the emulator with the Download emulator firmware mode.

Table 4.2 Program Area for the E7 Emulator

MCU Name	Program Area
H8/38327F	Flash memory: H'E000 to H'FFFF
H8/38347F	Internal RAM: H'F300 to H'F6FF Vector, etc.: H'0002 to H'0007, H'EFF8 to H'EFF9

11. The emulator uses a two-word stack pointer for values stored on a user program break. Therefore, the stack area must accept two-word addresses.
12. Do not use an MCU that has been used for debugging.
If the flash memory is rewritten many times, data may be lost due to retention problems after the emulator has been left for a few days and the data will be erased. If an error message is displayed, exchange the MCU for a new one.
13. Debugging of the emulator will not be normally performed if bit 2 of PCR3 and bit 2 of PDR3 are rewritten in the user program or the BSET.B and BCLR.B instructions are issued to other than bit 2 in each register.
14. **Restriction on Software Write Enable (SWE) Bit**
If the SWE bit is set to 1 during execution of the user program, a communication timeout error will occur. Do not set this bit to 1.

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