

To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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HITACHI SEMICONDUCTOR TECHNICAL UPDATE

Classification of Production	Development Environment		No	TN-OS*-068A/E	Rev	1
THEME	Release of HI7000/4 V1.0.04		Classification of Information	①. Spec change 2. Supplement of Documents 3. Limitation of Use 4. Change of Mask 5. Change of Production Line		
PRODUCT NAME	HS0700ITI41SRE, HS0700ITI41SRE-E, HS0700ITI41SRB, HS0700ITI41SRB-E, HS0700ITI41SRS, HS0700ITI41SRS-E	Lot No.	Reference Documents	HI7000/4 series User's Manual ADE-702-248 (Rev.1.0)		Effective Date
		V1.00r1, V1.01r1, V1.0Ar1, V1.0Br1, V1.0Cr1				Forever

The HI7000/4 V1.0.04 is released.

This version fixes following problems, and the problems which have been reported with the TECHNICAL UPDATE "HI7000/4 Bugs regarding cyclic handler and ref_alm"(TN-OS*-061A/E).

1. The ref_alm and iref_alm might enter endless loop.

[Generation Conditions]

(1) The alarm handler specified by ref_alm or iref_alm is in a starting state.

(2) When ref_alm or iref_alm is called, the starting time of the target alarm handler is later than the 12th it of all the alarm handlers in a starting state.

(3) From the interrupt handler or time-event handler which interrupts during ref_alm or iref_alm processing, ista_alm is called to the arbitrary alarm handlers in a starting state.

2. The ref_cyc and iref_cyc might enter endless loop.

[Generation Conditions]

(1) The cyclic handler specified by ref_cyc or iref_cyc is in a starting state.

(2) When ref_cyc or iref_cyc is called, the starting time of the target cyclic handler is later than the 11th it of all the cyclic handlers in a starting state.

(3) From the interrupt handler or time-event handler which interrupts during ref_cyc or iref_cyc processing, ista_cyc is called to the arbitrary cyclic handlers in a starting state and without TA_PHS attribute.

3. When an interruption handler which interrupt level is higher than the timer interruption level (CFG_TIMINTLEVEL) calls istp_cyc to the cyclic handler [A] which has already started or the cyclic handler [B] which has TA_PHS attribute, the start time of the cycle handler [B] or other cyclic handlers might shift.

Please download HI7000/4 V1.0.04 from the following URL.

URL : <http://www.hitachisemiconductor.com/sic/jsp/japan/eng/products/mpumcu/tool/download/itron.html>