

To our customers,

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## Old Company Name in Catalogs and Other Documents

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On April 1<sup>st</sup>, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

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Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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# HITACHI SEMICONDUCTOR TECHNICAL UPDATE

Classification of Production	Development Environment		No	TN-OS*-070A/E	Rev	1
THEME	HI7700/4 Problems	Classification of Information	1. Spec change 2. Supplement of Documents ③. Limitation of Use 4. Change of Mask 5. Change of Production Line			
PRODUCT NAME	HS0770ITI41SRE, HS0770ITI41SRE-E, HS0770ITI41SRB, HS0770ITI41SRB-E, HS0770ITI41SRS, HS0770ITI41SRS-E	Lot No. V1.00r1, V1.01r1, V1.0Ar1, V1.0Br1, V1.0Cr1, V1.1.00	Reference Documents	HI7000/4 series User's Manual ADE-702-248 (Rev.1.0)		Effective Date Forever

The HI7700/4 has following problems.

We will revise these problems, and the problems which have been reported with the TECHNICAL UPDATE "HI7700/4 and HI7750/4 Bugs regarding ref\_alm"(TN-OS\*-063A/E). We will release the revised version by the end of the Jan. 2003.

1. The ref\_alm and iref\_alm might enter endless loop.

[Generation Conditions]

(1) The alarm handler specified by ref\_alm or iref\_alm is in a starting state.

(2) When ref\_alm or iref\_alm is called, the starting time of the target alarm handler is later than the 12th it of all the alarm handlers in a starting state.

(3) From the interrupt handler or time-event handler which interrupts during ref\_alm or iref\_alm processing, ista\_alm is called to the arbitrary alarm handlers in a starting state.

[Countermeasure]

Don't use 12 or more alarm handler.

2. The ref\_cyc and iref\_cyc might enter endless loop.

[Generation Conditions]

(1) The cyclic handler specified by ref\_cyc or iref\_cyc is in a starting state.

(2) When ref\_cyc or iref\_cyc is called, the starting time of the target cyclic handler is later than the 11th it of all the cyclic handlers in a starting state.

(3) From the interrupt handler or time-event handler which interrupts during ref\_cyc or iref\_cyc processing, ista\_cyc is called to the arbitrary cyclic handlers in a starting state and without TA\_PHS attribute.

[Countermeasure]

Don't use 11 or more cyclic handler.

3. When an interruption handler which interrupt level is higher than the timer interruption level (CFG\_TIMINTLEVEL) calls istp\_cyc to the cyclic handler [A] which has already started or the cyclic handler [B] which has TA\_PHS attribute, the start time of the cycle handler [B] or other cyclic handlers might shift.

[Countermeasure]

Specify same value to the "Timer Interrupt Level (CFG\_TIMINTLVL)" and "Kernel Interrupt Mask Level (CFG\_KNLMSKLVL)".

Note, we will change though the release of the revised version was informed the end of December in 2002 in the TN-OS\*-063A/E in January, 2003.