

M16C R8C Cycle-Accurate Simulator Debugger beta2-version Release Notes

This document describes the notes of this debugger, and please read before you start to use this debugger.

And also, please refer to the “High-performance Embedded Workshop Release Notes” about the notes of High-performance Embedded Workshop IDE.

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1 Notes

1.1 Install

This installer over-writes existing M16C R8C simulator debugger. Therefore please install this product to a different directory from an existing High-performance Embedded Workshop. (In "Choice of an installation mode" page, please select the "Install a new High-performance Embedded Workshop".) If you use the compiler, please install the compiler package in the High-performance Embedded Workshop environment that installed this product.

Do not update the High-performance Embedded Workshop for the installed product in the future.

1.2 Executed time span and executed cycles

The bus width, the state of instruction queues, and the software wait for instructions are considered when measuring the number of cycles. The number of cycles, however, still might not match the result on the actual chip. To calculate the exact number of execution cycles, we recommend that you use the full-spec emulator or the compact emulator.

1.3 User's manual and Help file

Please refer to the cycle-accurate simulator debugger user's manual for the manual that describes new GUI for cycle-accurate feature.

We can NOT provide the Help file that describes new GUI for cycle-accurate feature.

1.4 Other

As for notes other than the above-mentioned, it is the same as the M16C R8C simulator debugger. Please refer to the release note of the M16C R8C simulator debugger for details.

2 System Requirements

2.1 M16C R8C Cycle-Accurate Simulator Debugger

Target host PC	
PC	IBM PC/AT compatible with Pentium III 600MHz or higher
OS	Windows XP Windows 2000
Memory	128MB or more (and in addition, ten or more times as much size as the load module file).
HDD	Hard disk available capacity for installation: 100MB or more. Prepare an area at least double the memory capacity (four-times or more recommended) as the swap area.
Display Resolution	1024 x 768 or higher recommended

3 Version Report

This section describes the specification of the changed software.

3.1 M16C R8C Cycle-Accurate Simulator Debugger beta2-version

In this version, the following specifications were changed from the previous version M16C R8C Cycle-Accurate Simulator Debugger beta-version.

3.1.1 Revisions to Restrictions

1. Fixed has been the problem that if the following conditions are all satisfied in the String Move instruction, the upper 1 byte of any 2-byte data cannot be transferred correctly:
 - An SMOVB or SMOVF instruction is used.
 - The size specifier is ".W".
 - The destination address of transfer is 0x10000 or later.
 - The above-mentioned address is an odd one.